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SPRUGR9D+++—January 2012

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Feedback TI Confidential—NDA

Restrictions Release History Release Date

Description/Comments D+++ January

2012 • Added Navigator Cloud note to Rx  
Flows. (Page 4-23) • Updated QMn Base  
Address Regs (Page 4-19)

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## Instruments

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mail.trempealeau.net Definition. •

Multicore Navigator is a collection of hardware components that facilitate data movement and multi core control. • The major components within the Navigator

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domain are: – A hardware Queue Manager (QM). – Specialized

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~~KeyStone Multicore Navigator | TI.com~~  
~~Video~~

My knowledge about Multicore Navigator is mostly limited with the Multicore Navigator for Dummies and user guide documents. So I have a couple of questions: 1) Is it possible to use Multicore Navigator for a complete infrastructure communication in a complete

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asymmetric system? If yes, I understand that we need to use QMSS TX queues and ...

~~[Resolved] Multicore Navigator for Complete Infrastructure ...~~

Multicore Navigator Question. Intellectual  
2025 points Hector Rivera ...

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~~Multicore Navigator Question – Processors  
forum ...~~

i have c6670 multicore messageQ example  
program its used for 4 cores. 1.how can i  
restrict into 2 core. 2.what are all the  
configuration changes in that example code  
?

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~~multicore navigator - Processors forum -  
Processors - TI ...~~

Since the Packet DMA uses the queue manager, it is assumed that you understand the concepts presented in Part 2 of the Multicore Navigator training. First we will discuss how the Packet DMA works. We'll talk about triggering, transmit processing,



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receive processing, and infrastructure processing.

~~KeyStone I training: multicore navigator—  
packet DMA ...~~

- Multicore Navigator User Guide (for Packet DMA) PKTDMA Controller SA PA GbE Switch Subsystem INTD Packet

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Streaming Switch Configuration Bus  
SGMII0 SGMII1 PHY stat\_pend\_raw[1:0]  
misc\_int buf\_starve\_intr  
mdio\_link\_intr[1:0] mdio\_user\_intr[1:0]  
PKTDMA.

~~Network Coprocessor for KeyStone Devices  
User's Guide~~

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Multicore Navigator User Guide - Texas Instruments multicore navigator: packet dma (pktdma) ... And you can see all the specific fields by looking at these registers in the Navigator User Guide. And lastly, for the receive DMA, the last register to be configured are the channel control registers.

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The only book to offer special coverage of the fundamentals of multicore DSP for implementation on the TMS320C66xx SoC. This unique book provides readers with an understanding of the TMS320C66xx SoC as well as its constraints. It offers critical

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analysis of each element, which not only broadens their knowledge of the subject, but aids them in gaining a better understanding of how these elements work so well together. Written by Texas Instruments' First DSP Educator Award winner, Naim Dahnoun, the book teaches readers how to use the development tools, take advantage of the

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maximum performance and functionality of this processor and have an understanding of the rich content which spans from architecture, development tools and programming models, such as OpenCL and OpenMP, to debugging tools. It also covers various multicore audio and image applications in detail. Additionally, this one-

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of-a-kind book is supplemented with: A rich set of tested laboratory exercises and solutions Audio and Image processing applications source code for the Code Composer Studio (integrated development environment from Texas Instruments) Multiple tables and illustrations With no other book on the market offering any

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coverage at all on the subject and its rich content with twenty chapters, *Multicore DSP: From Algorithms to Real-time Implementation on the TMS320C66x SoC* is a rare and much-needed source of information for undergraduates and postgraduates in the field that allows them to make real-time applications work in a



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relatively short period of time. It is also incredibly beneficial to hardware and software engineers involved in programming real-time embedded systems.

This book includes a range of techniques for developing digital signal processing code; tips and tricks for optimizing DSP software;

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and various options available for constructing DSP systems from numerous software components.

This book provides a comprehensive overview of the VLSI design process. It covers end-to-end system on chip (SoC) design, including design methodology, the

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design environment, tools, choice of design components, handoff procedures, and design infrastructure needs. The book also offers critical guidance on the latest UPF-based low power design flow issues for deep submicron SOC designs, which will prepare readers for the challenges of working at the nanotechnology scale. This practical guide

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will provide engineers who aspire to be VLSI designers with the techniques and tools of the trade, and will also be a valuable professional reference for those already working in VLSI design and verification with a focus on complex SoC designs. A comprehensive practical guide for VLSI designers; Covers end-to-end VLSI SoC

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design flow; Includes source code, case studies, and application examples.

Xcode Tools Sensei is a book about Apple's developer tools that are used to create Mac and iOS applications. This book doesn't stop with Xcode and Interface Builder. Xcode Tools Sensei covers a dozen

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developer tools, both graphical and command-line tools. You will learn how to profile your code and check for memory leaks with Instruments, write shaders with OpenGL Shader Builder, and uncover performance problems with OpenGL ES Performance Detective. If you want to spend more time creating, testing, and profiling

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your applications and less time wading through Apple's documentation, get a copy of Xcode Tools Sensei. This edition has been updated for Xcode 4.5 and iOS 6. Some of the new material in this edition includes auto layout for iOS applications, cherry picking commits, and creating base localizations to simplify application localization.

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Adobe Premiere Pro CC 2018 has been greatly improved for users, such as videographers, filmmakers and designers with several new features. Comparison view is one such feature that allows users to divide the view on the monitor to compare two separate clips or one clip before and after an



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effect has been added. In addition, users can also match colours from two separate shots in a sequence with just a touch of a button. Several graphic enhancements have also been added allowing users to browse for Motion Graphic templates in Adobe Stock, templates folder within Premiere Pro and in Creative Cloud Libraries. For

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videographers, auto-duck music will make the process of sound mixing in videos much easier and produces a professional sound.

This book is about the Zynq-7000 All Programmable System on Chip, the family of devices from Xilinx that combines an application-grade ARM Cortex-A9

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processor with traditional FPGA logic fabric. Catering for both new and experienced readers, it covers fundamental issues in an accessible way, starting with a clear overview of the device architecture, and an introduction to the design tools and processes for developing a Zynq SoC. Later chapters progress to more advanced topics

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such as embedded systems development, IP block design and operating systems.

Maintaining a 'real-world' perspective, the book also compares Zynq with other device alternatives, and considers end-user applications. The Zynq Book is accompanied by a set of practical tutorials hosted on a companion website. These

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tutorials will guide the reader through first steps with Zynq, following on to a complete, audio-based embedded systems design.

Popular Mechanics inspires, instructs and influences readers to help them master the

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modern world. Whether it ' s practical DIY home-improvement tips, gadgets and digital technology, information on the newest cars or the latest breakthroughs in science -- PM is the ultimate guide to our high-tech lifestyle.

An approachable, hands-on guide to

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understanding how computers work, from low-level circuits to high-level code. How Computers Really Work is a hands-on guide to the computing ecosystem: everything from circuits to memory and clock signals, machine code, programming languages, operating systems, and the internet. But you won't just read about these

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concepts, you'll test your knowledge with exercises, and practice what you learn with 41 optional hands-on projects. Build digital circuits, craft a guessing game, convert decimal numbers to binary, examine virtual memory usage, run your own web server, and more. Explore concepts like how to: •  
Think like a software engineer as you use



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data to describe a real world concept • Use Ohm's and Kirchhoff's laws to analyze an electrical circuit • Think like a computer as you practice binary addition and execute a program in your mind, step-by-step The book's projects will have you translate your learning into action, as you: • Learn how to use a multimeter to measure resistance,

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current, and voltage • Build a half adder to see how logical operations in hardware can be combined to perform useful functions • Write a program in assembly language, then examine the resulting machine code • Learn to use a debugger, disassemble code, and hack a program to change its behavior without changing the source code • Use a

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port scanner to see which internet ports your computer has open • Run your own server and get a solid crash course on how the web works And since a picture is worth a thousand bytes, chapters are filled with detailed diagrams and illustrations to help clarify technical complexities. Requirements: The projects require a variety of hardware -

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electronics projects need a breadboard, power supply, and various circuit components; software projects are performed on a Raspberry Pi. Appendix B contains a complete list. Even if you skip the projects, the book's major concepts are clearly presented in the main text.

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Presents an introduction to the new programming language for the Java Platform.

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