

## Harmonic Reduction Amplifier Using 4 High Impedance Bias

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Harmonic Reduction Amplifier using  $\frac{1}{4}$  High Impedance Bias Line with Defected Ground Structure (DGS) Abstract: In this paper, a new defected ground structure (DGS)  $\frac{1}{4}$  bias line that dumbbell-shaped ground pattern is etched on ground plane of microstrip line is proposed.

Harmonic Reduction Amplifier using  $\frac{1}{4}$  High Impedance Bias ...

By comparing the amplifier using the conventional  $\frac{1}{4}$  bias line with the amplifier using DGS  $\frac{1}{4}$  bias line, the 3rd harmonic signal level can be reduced about 26.51dB. Figure 7 shows photographs of the fabricated amplifier with DGS  $\frac{1}{4}$  bias line. Also figure 8 shows comparison results of the 3rd harmonic signal level between the

Harmonic Reduction Amplifier using  $\frac{1}{4}$  High Impedance Bias ...

Harmonic Reduction Amplifier Using 4 With harmonic reduction characteristics, efficiency and linearity of amplifier are improved. When the proposed bias line is adopted in power amplifier on IMT-2000 basestation transmitting band, the 3 rd harmonic signal is reduced about 26.5dB and efficiency is improved about 9.1% and IMD3 is improved 4.5dB ...

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Harmonic Reduction Amplifier using  $\frac{1}{4}$  High Impedance Bias ...

Harmonic Reduction Amplifier using  $\frac{1}{4}$  High Impedance Bias Line with Defected Ground Structure (DGS) By Do-Kyeong Hwang, Si-Gyun Jeong, Young-Pil Kwon, Yong-Chae Jeong and Chul-Dong Kim Get PDF (280 KB)

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Harmonic Reduction Amplifier Using 4 High Impedance Bias

harmonic in high frequency systems is a high power amplifier that results from non-linearity property of such amplifiers [3]. Traveling Wave Tube Amplifiers (TWTAs) are one of the most important high power and wideband amplifiers [4], so they have an important role in wideband communication systems, thus their harmonic levels should be controlled.

Second Harmonic Reduction of Traveling Wave Tube Amplifier ...

Using this method, harmonics of order 5, 7, 17, 19 Can be eliminated without affecting harmonics order of 11, 13, 23, 25, etc. 4. Several Harmonic Reduction can be eliminated by combining the phase displaced outputs of two inverters. This method has poor inverter utilisation and is not used.

Harmonic Reduction | Series addition of Inverter Output ...

This is part V of the video series of power amplifiers. This part describes harmonic distortion. It discusses how nonlinear behaviour of transistor in power ...

Power Amplifier (Part 5): Push Pull Class A Amplifier ...

Harmonic Reduction The output of a standard class E RF amplifier, as delivered to the output network is a very non-symmetrical, harmonic rich waveform. By using an even number of modules, it is possible to configure the amplifier to deliver a symmetrical, rounded waveform to the output network, which contains significantly fewer harmonics.