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Editor's Note: I was recently
perusing a new book called
Design Recipes for FPGAs that
was written by Peter Wilson and

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published by Newnes (ISBN-13: 978-0750668453). This is a rather interesting "Cook Book" jam-packed with "Design Recipes". Part 1 provides primers for FPGAs, VHDL, and standard design flows.

Design Recipes for FPGAs – A

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Chapter 1 Introduction Abstract

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design optimization where the performance of a design can be made drastically better than an initial naive implementation.

Before discussing details of how to make the designs optimal for the individual goals of speed, area and power (the “big three”

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for design optimization generally in...

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Design Recipes for FPGAs doesn't talk about this, probably since it's aimed at people building

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hardware. Be warned; HDLs are very different from programming languages. FPGA design has a number of steps – the testbench, compilation, synthesis, routing, etc. and Mr. Wilson does take the reader through the design flow.

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