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ASICs, ...)

Mark Zuckerberg in
conversation with Stanford President
John Hennessy Introduction to
Computers - Lesson 1 - The CPU

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(EECS2021E) - Chapter 4 (Part I) -

Basic Logic Design John Hennessy
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6.823 Spring 2020 Reading List

Optional readings are NOT required for the class, but interested students are always encouraged to check them out for in-depth understanding of the topics. Readings refer to: H&P6:

Computer Architecture: A Quantitative Approach, 6th edition, by Hennessy and Patterson
H&P5: Computer Architecture: A Quantitative Approach, 5th edition, by Hennessy and Patterson

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There are several correct answers.

One would be that, with the current

sys- tem, one computer fails

approximately every 5 minutes. 5

minutes is unlikely to be enough time

to isolate the computer, swap it out,

and get the computer back on line

again. 10 minutes, however, is much

more likely.

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Performance], 6th Edition, Pearson Education, 2003. 3. Hayes, J.P.,

Computer Architecture and Organization], 3rd Edition, Tata McGraw Hill, 1998. UNIT I BASIC

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Functional units Basic operational concepts Bus structures

~~CS1252 COMPUTER ORGANIZATION AND ARCHITECTURE~~

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Sixth Edition, Portland, OR Addresses fundamental principles in computer organization and architecture and the critical role of performance in driving computer design, covering superscalar design, IA-64 design features, and parallel processor ...

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Emulation & Simulation Readings. A Look at Computer Architecture Evaluation Methodologies Mario Badr and Natalie Enright Jerger; Workshop on Pioneering Processor Paradigms (WP3) 2018

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